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Sliding mode approach for control and observation of a three phase AC-DC pulse-width modulation rectifier

Introduction. For AC-DC conversion systems, the electrical systems typically use thyristor or diode bridge rectifiers, which have relatively poor performance. Nowadays, three-phase pulse-width modulation rectifiers are widely applied in various applications for their well-known intrinsic benefits, such as adjustable DC link voltage, unity power factor, bidirectional power flow and very low total harmonic distortion. Purpose. The objective of this work is to achieve better stability and dynamic performance using sliding mode strategy for control and observation. Methods. For that purpose, first a sliding mode controller is introduced on the DC-link side to ensure a fast and accurate response of the output load voltage. Then, the sliding mode approach is employed to control the quadrature and direct components of power to maintain the input power factor at unity. Finally, this approach is used to design two observers for grid voltage estimation and online variation of load resistance. To overcome the problem associated with the use of the classical low-pass filter, an adaptive compensation algorithm is used to compensate the attenuation of the amplitude and phase delay of the observed grid voltages. This algorithm is based on the use of the two low-pass filters in cascade and ensures the minimization of chattering. Results. Comparative studies have been carried out between sliding mode control method for controlling the three-phase AC-DC pulse-width modulation rectifier and other conventional techniques. The validation by simulation and the tests carried out gave very satisfactory results and proved the effectiveness and feasibility of the sliding mode for both control and observation of three phase pulse-width modulation rectifier. References 25, table 2, figures 19.

Key words: direct power control, three phase pulse-width modulation rectifier, sliding mode control, double-low pass filter, unity power factor.

Вступ. Для АС-DC систем перетворення електричні системи зазвичай використовують тиристорні або діодні мостові випрямлячі, які мають відносно погані характеристики. В даний час трифазні випрямлячі з широтно-імпульсною модуляцією широко застосовуються з різними цілями завдяки їх добре відомим внутрішнім перевагам, таким як регульована напруга у ланці постійного струму, одиничний коефіцієнт потужності, двонаправлений потік потужності та дуже низькі загальні гармонічні спотворення. Метою даної роботи є досягнення кращої стабільності та динамічних характеристик з використанням стратегії ковзного режиму для контролю та спостереження. Методи. З цією метою спочатку на стороні ланки постійного струму вводиться регулятор режиму ковзання, шоб забезпечити швидку і точну реакцію на вихідну напругу навантаження. Потім використовується метод ковзного режиму для управління квадратурною та прямою складовими потужності, щоб підтримувати вхідний коефіцієнт потужності рівним одиниці. Нарешті цей підхід використовується для розробки двох спостерігачів для оцінки напруги мережі та зміни опору навантаження в режимі онлайн. Для подолання проблеми, пов'язаної з використанням класичного низькочастотного фільтру, використовується алгоритм адаптивної компенсації, що компенсує загасання амплітуди і фазової затримки напруг мережі, що спостерігаються. Цей алгоритм заснований на використанні двох низькочастотних фільтрів у каскаді та забезпечує мінімізацію брязкоту. Результати. Були проведені порівняльні дослідження між методом керування ковзним режимом для керування трифазним випрямлячем АС-DC з широтно-імпульсною модуляцією та іншими традиційними методами. Перевірка за допомогою моделювання та проведені випробування дали дуже задовільні результати та довели ефективність та здійсненність ковзного режиму як для управління, так і для спостереження за трифазним випрямлячем з широтно-імпульсною модуляцією. Бібл. 25, табл. 2, рис. 19.

Ключові слова: пряме керування потужністю, трифазний випрямляч з широтно-імпульсною модуляцією, ковзне керування, подвійний низькочастотний фільтр, одиничний коефіцієнт потужності.

Introduction. In recent years, advances in the field of power electronics have contributed to the development of new static converter structures that allow the modulation of electrical energy according to industrial use. These converters are widely used in industrial or domestic fields, such as non-controlled diodes and controlled thyristor rectifiers. These converters behave like non-linear loads, absorbing harmonic currents that have a very negative impact on the electrical grid. Indeed, it can cause a number of disturbances, ranging from equipment dysfunction to complete destruction.

Harmonic pollution is a phenomenon that leads to the degradation of power quality, especially the distortion of the voltage wave, which increases the rate of harmonic distortion of the current that can exceed a value of 30 % [1]. Unfortunately, this value does not conform to the international standards imposed by the specialized organisms: IEC 61000, EN 50160 and IEEE Standard 519 which impose total harmonic distortion (THD) limits of 3 % for voltages and 5 % for currents [1]. In this situation, and to overcome this kind of problem, several solutions have been developed: curative solutions such as active filters and preventive solutions such as pulse width modulation rectifiers. Low harmonic pollution, controllable reactive

power, adjustable DC-link voltage, unit power factor and bi-directional power flow are some advantages of this converter topology [2-7].

Because of its higher performances than the traditional thyristor or diode rectifier bridge, the three-phase pulse-width modulation (PWM) rectifier is now widely utilized in industry, including power supplies for microelectronics, wind power production, telecommunications equipment, etc [8]. The design of these non-polluting converters, combined with a judicious choice of control, provides a significant improvement current waveform of the electrical grid and guarantees operation with a unity power factor.

In this context, many techniques have been applied to three-phase PWM rectifiers to improve power quality. Voltage-oriented control, which derives from field-oriented control applied to AC machines, is one of the efficient techniques that permit decoupled control. It consists of eliminating the voltage quadrature component in order to ensure an indirect and decoupled control of the power. The voltage references for the direct and quadrature components are generated from the current controllers in the rotating frame. Since position data is necessary for all d-q transformations in voltage-oriented

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control, position information can be obtained via estimation of the virtual flux or from measurement or observation of grid voltages. As for the reference of the direct current component, it is obtained from the regulation of DC-link voltage, while the reference of the quadrature component is set to zero in order to have a unitary power factor operation.

By analogy to the direct torque control of AC machines in the stationary α - β frame, another control technique for PWM rectifiers proposed by Ohnishi in 1991 so-called direct power control (DPC) and later developed by Takahachi and Noguchi in 1998 [9, 10].

In DPC, it is possible to control directly the active and reactive power by selecting the appropriate inverter state from a predefined switching table. For each sampling time, the appropriate inverter voltage vectors are selected according to the errors between the active and reactive powers and their references and the position of the voltage vector. The active power reference is provided by a PI controller for DC-link voltage while the reactive power reference is maintained at zero to ensure unity power factor. Two DPC configurations can be used: DPC based on measured or estimated voltage vectors (V-DPC) [1, 9, 11-12]: the position is determined by voltage vectors, and the instantaneous powers are calculated from voltage and current vectors. DPC based on virtual flux [1, 13-16]: where the estimated fluxes define the position and the instantaneous powers are calculated from the current and fluxes vectors. However, the classical direct power control has many disadvantages, such as: a variable switching frequency due to the hysteresis comparators, a high filter inductance value to obtain a smooth current, a low sampling period is required for accurate power estimation, and its implementation requires a fast microprocessor and an ADC due to its high sampling frequency requirement [17].

In order to improve the classical DPC, this strategy is combined with the sliding mode control (SMC) approach, which is distinguished by its robustness to parametric variations and external perturbations [18, 19]. In addition, the SMC is used to design simple, efficient and more economical observers which eliminates the need for physical sensors and reduces cost.

In this perspective, this paper aims to improve the dynamic and static performances of direct power control using the sliding mode approach. This improvement can be illustrated by comparison with other techniques in terms of THD and power factor. To achieve these objectives, enhancements have been introduced by:

- substituting the PI controller of the DC-link voltage with SMC;
- ensuring direct and decoupled control for both active and reactive power;
- replacing grid voltage sensors with a sliding mode observer combined with a double-low-pass filter to minimize the chattering phenomenon and to compensate amplitude attenuation and phase errors;
 - on-line estimating variation in load resistance.

Three phase PWM rectifier model. A three-phase PWM rectifier's topology is given by Fig. 1, where i_a , i_b , i_c and e_a , e_b , e_c are the three-phase grid currents and voltages, V_a , V_b , V_c are the input voltages of three-phase PWM rectifier, L and R are the inductance and resistance of interconnecting filters and R_L is the load resistor of the DC side.

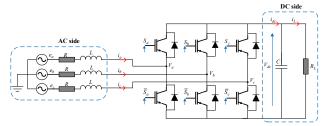


Fig. 1. Electrical circuit of a three phase PWM rectifier

According to the per phase equivalent circuit in Fig. 2, the AC side equations can be given as:

$$\begin{cases}
L\frac{di_{a}}{dt} = e_{a} - Ri_{a} - V_{a}; \\
L\frac{di_{b}}{dt} = e_{b} - Ri_{b} - V_{b}; \\
L\frac{di_{c}}{dt} = e_{c} - Ri_{c} - V_{c}.
\end{cases}$$

$$\downarrow V_{i}$$

Fig. 2. Per phase model of three phase PWM rectifier

For the DC side, the equation is written as:

$$C\frac{dV_{dc}}{dt} = (S_a i_a + S_b i_b + S_c i_c) - \frac{V_{dc}}{R_L}.$$
 (2)

In the (α, β) frame, the three-phase boost rectifier model can be written as:

$$\begin{cases} L \frac{\mathrm{d}i_{\alpha}}{\mathrm{d}t} = e_{\alpha} - Ri_{\alpha} - V_{\alpha}; \\ L \frac{\mathrm{d}i_{\beta}}{\mathrm{d}t} = e_{\beta} - Ri_{\beta} - V_{\beta}; \\ C \frac{\mathrm{d}V_{dc}}{\mathrm{d}t} = (S_{\alpha}i_{\alpha} + S_{\beta}i_{\beta}) - \frac{V_{dc}}{R_{I}}. \end{cases}$$
(3)

SMC-based DPC of three phase PWM rectifier. The block scheme of SMC-based DPC is given in Fig. 3. Sliding mode controllers are utilized to regulate the DC-link voltage, active power, and reactive power.

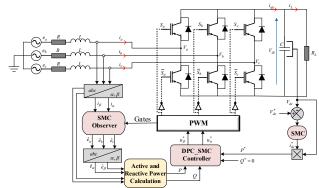


Fig. 3. DPC-SMC of three phase PWM rectifier

DC-link voltage sliding mode control. To determine the sliding surface, the general form is given as [10]:

$$S(t) = \left(\lambda + \frac{\mathrm{d}}{\mathrm{d}t}\right)^{n-1} e(t) + K \int_0^t e(t) \mathrm{d}t, \qquad (4)$$

where e(t) is the error between the reference and controlled values; λ , K are the strictly positive constants; n is the relative degree, which corresponds to the number of differentiations required for the output, until the control appears. In our case, n is set to 1, which gives the following sliding mode surface of DC-link voltage:

$$S_{dc}(t) = e_{dc}(t) + K_1 \int e_{dc}(t) dt, \qquad (5)$$

where K_1 is the positive gain and:

$$e_{dc}(t) = V_{dc}^{*}(t) - V_{dc}(t), \tag{6}$$

by taking the first derivative of the sliding surface, we have:

$$\dot{S}_{dc} = \dot{e}_{dc} + K_1 e_{dc} = \dot{V}_{dc}^* + \frac{V_{dc}}{R_L C} + K_1 \left(V_{dc}^* - V_{dc} \right) - \frac{1}{C} i_{dc} . \tag{7}$$

In steady state, $\dot{S}_{dc} = S_{dc} = 0$ which gives:

$$i_{dc}^* = C\dot{V}_{dc}^* + \frac{V_{dc}}{R_L} + K_1C(V_{dc}^* - V_{dc}) + K_{dc}\text{sign}(S_{dc}),$$
 (8)

where K_{dc} is the positive constant and the function $sign(S_{dc})$ is substituted by a smooth function (saturation), to minimize the chattering effect. It is given as:

$$sat(S_{dc}) = \begin{cases}
+1 & \text{if } S_{dc} > \gamma; \\
-1 & \text{if } S_{dc} < \gamma; \\
\frac{S_{dc}}{\gamma} & \text{if } |S_{dc}| \le \gamma,
\end{cases} \tag{9}$$

where γ is the smooth factor.

Stability analysis. Let us take the Lyapunov's function as follows:

$$V_c = \frac{1}{2} S_{dc}^2 \ . \tag{10}$$

The time derivative of this function is:

$$\dot{V}_c = S_{dc} \dot{S}_{dc} . \tag{11}$$

Substituting the derivative of the surface with its expression, we get:

$$\dot{V_{c}} = S_{dc} \begin{pmatrix} \dot{V}_{dc}^{*} - \left(\dot{V}_{dc}^{*} + \frac{V_{dc}}{R_{L}C} + K_{1} \left(V_{dc}^{*} - V_{dc} \right) \right) \\ - \frac{K_{dc}}{C} \operatorname{sign}(S_{dc}) + \frac{V_{dc}}{CR_{L}} + K_{1} \left(V_{dc}^{*} - V_{dc} \right) \end{pmatrix}, \quad (12)$$

$$\dot{V}_c = -S_{dc} \frac{K_{dc}}{C} \operatorname{sign}(S_{dc}) = -\frac{K_{dc}}{C} |S_{dc}|.$$
 (13)

For \dot{V}_c <0, K_{dc} must be a positive constant. Finally the reference of active and reactive powers can be written as:

$$\begin{cases} P^* = V_{dc}i_{dc}; \\ Q^* = 0. \end{cases}$$
 (14)

where i_{dc} is the DC-link current.

Active and reactive powers control. In this part, sliding mode controllers are introduced to ensure that the powers track their imposed desired values. The sliding surfaces of active and reactive powers are given as:

$$\begin{cases} S_P = e_P + K_2 \int e_P dt; \\ S_Q = e_Q + K_3 \int e_Q dt, \end{cases}$$
 (15)

where:

$$\begin{cases}
e_p = P - P^*; \\
e_Q = Q - Q^*,
\end{cases}$$
(16)

where K_2 and K_3 are the positive gains. The surface derivatives are given as:

$$\begin{cases} \dot{S}_P = \dot{e}_P + K_2 e_P; \\ \dot{S}_O = \dot{e}_O + K_3 e_O, \end{cases}$$
 (17)

where:

$$\begin{cases} P = e_{\alpha}i_{\alpha} + e_{\beta}i_{\beta}; \\ Q = e_{\beta}i_{\alpha} - e_{\alpha}i_{\beta}. \end{cases}$$
 (18)

Substituting active and reactive powers by their expressions and calculating the derivative of surface functions in matrix format, we can obtain:

$$\dot{S}_{PO} = F + DU \,, \tag{19}$$

where:

$$D = \begin{bmatrix} -\frac{e_{\alpha}}{L} & -\frac{e_{\beta}}{L} \\ -\frac{e_{\alpha}}{L} & \frac{e_{\beta}}{L} \end{bmatrix}, \quad U = \begin{bmatrix} V_{\alpha} \\ V_{\beta} \end{bmatrix} \text{ and }$$

$$F = \begin{bmatrix} \dot{V}_{\alpha}i_{\alpha} + \dot{V}_{\beta}i_{\beta} + \left(K_{2} - \frac{R}{L}\right)P + \frac{1}{L}\left(V_{\alpha}^{2} + V_{\beta}^{2}\right) - \dot{P}^{*} - K_{2}P^{*} \\ \dot{V}_{\beta}i_{\alpha} - \dot{V}_{\alpha}i_{\beta} + \left(K_{3} - \frac{R}{L}\right)Q - \dot{Q}^{*} - K_{3}Q^{*} \end{bmatrix},$$

where U is the vector control and F is the independent term. In a steady state, to obtain the equivalent control term:

$$\dot{S}_{PQ} = 0. \tag{20}$$

So:

$$U_{eq} = -D^{-1}F \ . {21}$$

Finally, the control vector is given as:

$$U = U_{eq} + U_{NL} = -D^{-1} [F + K_{PO} sign(S_{PO})].$$
 (22)

The Lyapunov's function derivative is given by:

$$V_{PQ} = S_{PQ}\dot{S}_{PQ} =$$

$$= S_{PQ}(F + DU) =$$

$$= S_{PQ}(F + D\left[-D^{-1}\left[F + K_{PQ}sign(S_{PQ})\right]\right]$$
(23)

Simplification gives:

$$\dot{V}_{PQ} = -K_{PQ} \left| S_{PQ} \right|, \tag{24}$$

so \dot{V}_{PO} < 0 only if K_{PQ} is the positive constant.

SMC-based grid voltages observer. From the dynamical model of three phase boost rectifier in (α, β) frame, the current model can be given as:

$$\frac{\mathrm{d}i_g}{\mathrm{d}t} = \frac{1}{L} \left(e_g - Ri_g - V_g \right),\tag{25}$$

where $i_g = [i_\alpha i_\beta]^T$, $e_g = [e_\alpha e_\beta]^T$ and $V_g = [V_\alpha V_\beta]^T$.

The sliding mode observer model is expressed as:

$$\frac{\mathrm{d}\hat{i}_g}{\mathrm{d}t} = \frac{1}{L} \left(G \operatorname{sign}(S_g) - R \hat{i}_g - V_g \right), \tag{26}$$

where $S_g = i_g - \hat{i}_g$ is the observer's sliding surface of the

current, $\hat{i}_g = \begin{bmatrix} \hat{i}_{\alpha} & \hat{i}_{\beta} \end{bmatrix}^T$ is the estimated current vector, $G = \begin{bmatrix} G_1 & G_2 \end{bmatrix}^T$ and G_1 , G_2 are the positive gains.

From the previous equations (25), (26), we get:

$$\frac{\mathrm{d}S_g}{\mathrm{d}t} = \frac{1}{L} \left(e_g - RS_g - G \mathrm{sign}(S_g) \right),\tag{27}$$

when sliding mode is reached $\dot{S}_g = 0$, estimated grid voltages can be expressed as:

$$e_g = G \operatorname{sign}(S_g). \tag{28}$$

A low-pass filter (LPF) that also reduces the impact of the chattering phenomenon can be used to produce the grid voltages.

$$e_{gf} = LPF(Gsign(S_g)), \tag{29}$$

where $e_{gf} = \begin{bmatrix} e_{\alpha f} & e_{\beta f} \end{bmatrix}^T$ is the observer's filtered output in (α, β) frame.

The use of the LPF to minimize the chattering of a sliding mode observer has the disadvantage of attenuating the amplitude of the observed signal and creating a phase delay between the estimated and the signal to be estimated. To remedy this problem in [20] authors have proposed an adaptive algorithm to compensate the attenuation caused on the amplitude and the phase delay. This algorithm also has the advantage of being insensitive to the variation of the frequency of the grid voltage, which improves the precision of the observation illustrated in Fig. 4.

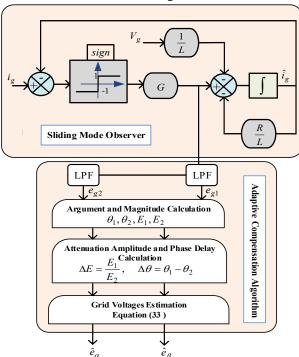


Fig. 4. Sliding mode-based grid voltages observer

The strategy of this algorithm is based on the use of two identical filters of low-pass type in a cascade (with same cut-off frequency ω_c).

$$\begin{cases} e_{g1} = \frac{\omega_c}{s + \omega_c} e_{gf}; \\ e_{g2} = \frac{\omega_c}{s + \omega_c} e_{g1}. \end{cases}$$
(30)

Amplitudes and arguments of outputs of previous filters can be calculated as follows:

$$\begin{cases} E_1 = \|e_{g1}\|; & \theta_1 = \arg(e_{g1}); \\ E_2 = \|e_{g2}\|; & \theta_2 = \arg(e_{g2}) \end{cases}$$

$$(31)$$

The errors in amplitudes and phases can be deduced as:

$$\begin{cases}
\Delta E = \frac{E_1}{E_2}; \\
\Delta \theta = \theta_1 - \theta_2.
\end{cases}$$
(32)

Finally, we can get the expressions of the estimated grid voltages by taking into account the compensation of the amplitude attenuation and the phase deviation, as:

$$\begin{cases} \hat{e}_{\alpha} = E_2 \Delta E^2 \cos(\theta_2 + 2\Delta\theta); \\ \hat{e}_{\beta} = E_2 \Delta E^2 \sin(\theta_2 + 2\Delta\theta). \end{cases}$$
(33)

Load resistance observer [21]. From the DC side voltage equation, the following two equations can be written as:

$$\begin{cases} \frac{\mathrm{d}V_{dc}}{\mathrm{d}t} = \frac{1}{C} (S_{\alpha}i_{\alpha} + S_{\beta}i_{\beta}) - \frac{V_{dc}}{CR_{L}}; \\ \frac{\mathrm{d}\hat{V}_{dc}}{\mathrm{d}t} = \frac{1}{C} (S_{\alpha}i_{\alpha} + S_{\beta}i_{\beta}) - \frac{\hat{V}_{dc}}{CR_{L0}} + \lambda \mathrm{sign}(e_{V}), \end{cases}$$
(34)

where \hat{V}_{dc} is the observed DC-link voltage and R_{L0} is the nominal value of load resistance, the error observation is given as:

$$e_V = V_{dc} - \hat{V}_{dc} \,. \tag{35}$$

In addition, the error dynamic equation:

$$\frac{\mathrm{d}e_{V}}{\mathrm{d}t} = \frac{\mathrm{d}V_{dc}}{\mathrm{d}t} - \frac{\mathrm{d}\hat{V}_{dc}}{\mathrm{d}t} =$$

$$= -\left(\frac{V_{dc}}{CR_{L}} - \frac{\hat{V}_{dc}}{CR_{L0}}\right) - \lambda \mathrm{sign}(e_{V}), \tag{36}$$

where λ is the positive constant that satisfies:

$$\lambda > \left| -\left(\frac{V_{dc}}{CR_L} - \frac{\hat{V}_{dc}}{CR_{L0}}\right) \right|, \tag{37}$$

when sliding mode is reached in finite time, equivalent control:

$$V_{dc} = \hat{V}_{dc} \Rightarrow \left(\frac{\hat{V}_{dc}}{CR_L} - \frac{V_{dc}}{CR_{L0}}\right) = V_{R,eq},$$
 (38)

where $V_{R,eq}$ is the equivalent control [22, 23] of the observer. Its estimate $\hat{V}_{R,eq}$ can be generated by the LPF:

$$\hat{V}_{R,eq} = LPF(\lambda sign(e_V)). \tag{39}$$

Finally, the estimated load resistance can be deduced from its nominal value and the observer's output:

$$\hat{R}_{L} = -\frac{C\hat{V}_{R,eq}}{\hat{V}_{dc}} + R_{L0} . {40}$$

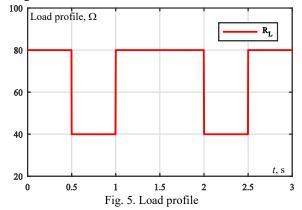
Simulation results and discussions. To illustrate the effectiveness and the feasibility of the enhanced DPC based on sliding mode approach for control and observation, simulation studies were carried out with MATLAB/Simulink under several operations conditions. For this purpose, all three-phase PWM rectifier parameters are summarized in Table 1.

Table 1

Three phase PWM rectifier parameters

Three phase I WWI recurrer parameters	
Parameters	Values
Grid frequency f, Hz	50
Grid voltage <i>V</i> , V	120
Reference of DC-link voltage V_{dc}^* , V	300
Grid side inductance <i>L</i> , mH	16
Grid side resistance R , Ω	0.1
DC-link capacitor C, μF	1100
Load resistance R_L , Ω	40→80
PWM frequency f_P , kHz	15

Several tests were carried out using MATLAB/Simulink to test the performance of DPC control based on SMC. In the first test, and for a variable load profile depicted by Fig. 5 the DPC-SMC block diagram is simulated for two operating conditions: with grid voltage measurement from 0 to 1.5 s and then without grid voltage sensor using a sliding mode observer from 1.5 s to 3 s.



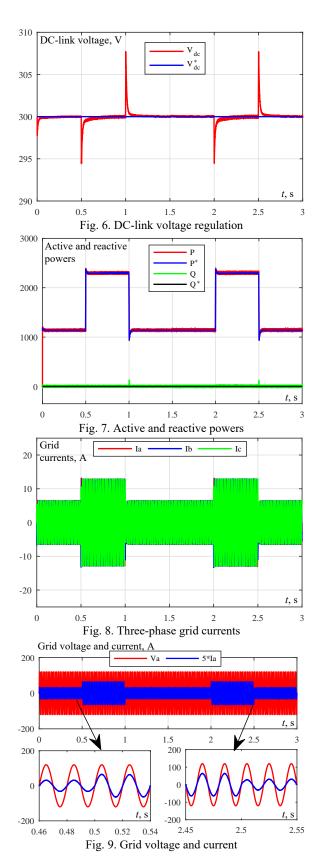
The curves obtained are illustrated by Fig. 6–11 and the following comments can be carried out.

In the presence of disturbance and according to Fig. 6, the DC-link voltage tracks perfectly its imposed reference (300 V) with a disturbance rejection considered good at the moments of load variation.

Figure 7 shows the evolution of active and reactive power. It is very clear that the active power has behavior identical to that of the reference generated reflecting the applied load. It is equal to 1195 W for $R = 80 \Omega$ and then increases to 2300 W when the load passes to $R = 40 \Omega$. On the same Fig. 7, it can be seen that reactive power has a zero average value, which guarantees a unitary power factor, and therefore the grid current and voltage are always in phase (Fig. 9).

Under a variable load profile, Fig. 10 shows the waveforms of the three voltages measured and estimated using a sliding mode-based observer. Whatever the operating conditions, the introduction of the voltage observer at time 1.5 s gives good results and the estimated grid voltages show no amplitude attenuation or phase delay compared to the actual signal. This is justified by the introduction of a compensation algorithm.

As for the power quality, for both with and without grid voltage observer operating conditions, the THD is less than 5 %, which is in accordance with IEEE 519 International Standards Fig. 11 for various operating conditions.



Another test is done in open loop to illustrate the advantages of the observer associated with double LPFs and a compensation algorithm. Examination of Fig. 12, 13 shows that the errors in amplitude and phase are very noticeable when the observer uses a single LPF. At time $t=0.5\,\mathrm{s}$, the system based on double LPFs and a compensation algorithm is introduced. It is easy to notice that the amplitude attenuation and the phase delay can be compensated.

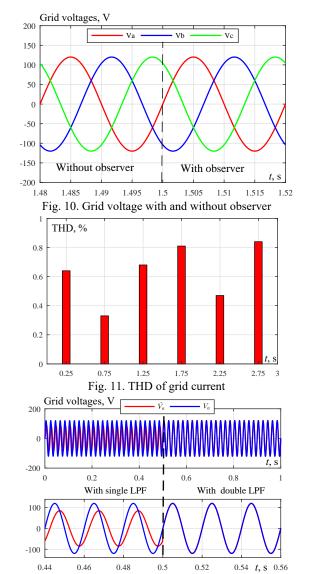
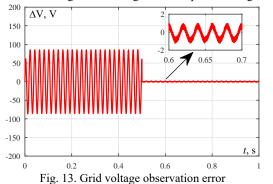


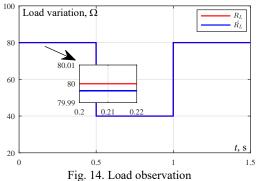
Fig. 12. Observed grid line voltage with compensation algorithm



The second test evaluates the DPC-SMC without a grid voltage sensor and with online estimation of the load variation using a sliding mode observer and under a load variable profile.

The results show that the performance of the sensorless DPC-SMC control is always better. Figure 14 shows that the observer accurately estimates the load variation which is reflected by the low estimation errors shown in Fig. 15.

In Fig. 16, the measured the DC-link voltage tracks perfectly its reference, which confirms the good design of the voltage controller.



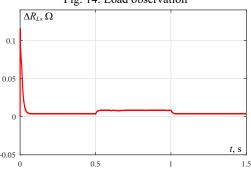


Fig. 15. Error in load estimation

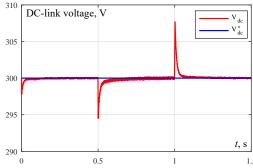


Fig. 16. Behavior of DC-link voltage

For Fig. 17 depicting the evolution of the instantaneous active and reactive powers, it is very clear that they present a fast dynamic and track their references without overshoot.

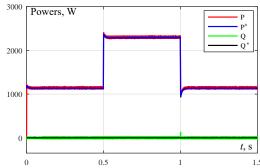


Fig. 17. Instantaneous active and reactive power

The high quality of the grid line current (sinusoidal waveform) is confirmed by a good unit power factor, as shown in Fig. 19, and by the low THD values indicated by the bars in Fig. 18 for various operating conditions.

Table 2 presents a comparison between DPC-SMC and other control techniques based on several terms.

Table 2

Comparison between conventional control strategies and DPC-SMC DPC-SMC DPC [24] Other techniques [25] THD, % 1.13 3.17 $3.35 \rightarrow 12.2$ Power factor $0.98 \rightarrow 0.99$

From this comparative table, the DPC-SMC control shows its superiority over the other techniques.

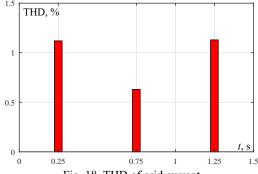


Fig. 18. THD of grid current

Power factor

1 0.995
0.99
0.98
0.97
0 0.5 1 1.5

Conclusions.

In this paper, the classical direct power control has been combined with a sliding mode control to control active and reactive power and to ensure an accurate regulation of DC-link voltage. To eliminate the need for voltage sensors and to get the online variation of load resistance, sliding mode based observers have been designed for this purpose. Through a series of tests, especially in the presence of disturbances, it has been shown that:

Fig. 19. Power factor

- 1. The sliding mode offers a clear improvement to the DC-link voltage regulation regardless of the load profile.
- 2. The combination of the sliding mode approach with direct power control has eliminated the need for a switching table, thus reducing the control complexity.
- 3. High performance instantaneously decoupled power control achieved using a sliding mode control which provides a unity power factor.
- 4. The sliding mode-based grid voltage observer combined with the compensation algorithm provided accurate results with minimum chattering compared to the single low-pass filter case.
- 5. A good power quality is justified by a lower total harmonic distortion value.
- 6. The direct power control sliding mode control gives better performance than conventional techniques with a total harmonic distortion value less than 5 % and a power factor close to unity.

Conflict of interest. The authors declare that they have no conflicts of interest.

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