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A power quality enhanced for the wind turbine with sensorless direct power control under different input voltage conditions

Introduction. The quality of electrical energy is essential during disturbances, at the level of power electronic devices will suffer serious operating problems causing dangerous damage. **Aim.** A new approach to direct power control without grid voltage sensor improves the quality and control of instantaneous active and reactive power converters. **Methodology.** First, the technique without network voltage sensor with a direct power control based on a switching table, which is a classic approach, is discussed and its performance is analyzed under increasing and decreasing load. In addition, the performance of the proposed technique is also analyzed under the same circumstances and their performance is compared. **Originality.** The new method consists of a nonlinear grid voltage modulated controller and a conventional controller which guarantees very good results in a polluted network. The proposed method is verified using MATLAB/Simulink. **Results.** The simulation results under different input conditions show that the proposed method not only has good tracking performance in active and reactive power, but also reduces the current total harmonic distortion to 1.9 %, which is good lower than the requirement for network operation. References 17, tables 2, figures 11. **Key words:** direct power control, virtual flux, grid voltage modulated, constant switching frequency.

Вступ. Якість електроенергії має важливе значення під час збоїв, на рівні силових електронних пристроїв будуть мати місце серйозні проблеми експлуатації, що викликають небезпечні пошкодження. **Мета.** Новий підхід до прямого керування потужністю без датчика напруги мережі покращує якість та контроль перетворювачів миттєвої активної та реактивної потужності. **Методологія.** Спочатку обговорюється методика без датчика напруги мережі з прямим керуванням потужністю на основі таблиці перемикання, що є класичним підходом, та аналізується його продуктивність при збільшенні та зменшенні навантаження. Крім того, ефективність запропонованої методики також аналізується за тих же обставин, і їхня продуктивність порівнюється. **Оригінальність.** Новий метод складається з нелінійного мережевого контролера з модуляцією напруги і звичайного контролера, який гарантує дуже хороші результати в забрудненій мережі. Запропонований метод перевіряється за допомогою MATLAB/Simulink. **Результати.** Результати моделювання за різних умов вхідної напруги показують, що запропонований метод не тільки має хороші характеристики відстеження активної та реактивної потужності, але також знижує поточний THD до 1,9 %, що значно нижче, ніж вимоги для роботи мережі. Бібл. 17, табл. 2, рис. 11.

Ключові слова: пряме керування потужністю, віртуальний потік, модуляція напруги мережі, постійна частота перемикання.

1. Introduction. Grid-connected converters are widely used in the application of smart grids, Flexible Alternating Current Transmission Systems (FACTS) and renewable energy sources (e.g., wind and solar) and various control methods have been investigated so that converters improve their performance [1].

Many studies have focused on the advanced control of the functioning of renewable energies that have been proposed with the same main objective which is the power quality which focuses on an almost sinusoidal input current waveform with a higher power factor and regardless of the input condition whether balanced or distorted but their different principles.

The instantaneous power theory proposed by Japanese researchers [2] is inspired by the Direct Torque Control (DTC) proposed for asynchronous motors [3]. The Direct Power Control (DPC) strategy has received a lot of attention from researchers in recent years due to its many benefits. They have shown that the use of DPC in various converters and applications whether it be matrix [4] or Voltage Source Converter (VSC) at two and three levels based on Fuzzy-Q-Learning algorithm is applied [5] or on active filters [6] is more advantageous.

A DPC switching table has been formulated in which the appropriate switching states are selected from a predefined optimal switching table based on the digitized signals of the instantaneous errors between the commanded and estimated values of active and reactive power extracted by a hysteresis controller and the angular position of the voltage at the terminals of the converter thus guarantees a decoupled control of the powers and good dynamic performance [7].

However, the varying switching frequency results in broadband harmonic spectra, which complicates the design of line filters. To solve the downshift associated with variable frequency operation, various DPC algorithms have been developed for constant switching frequency. One can find various publications studies on how to fix the switching frequency of the DPC. The authors [8] suggest to associate the principle of the DPC with a vector modulation Space Vector Modulation (SVM) in order to obtain a switching frequency constant without the use of a switching table. Other authors [9] combine other technique with DPC to improve the result even more by using Sliding-Mode Control (SMC) it allows to obtain a stabilization response faster than that of the PI controller and a greater robustness. Backstepping improves transient performance [10]. The SVM modulation based on a predictive controller was developed in [11] and even the new method proposed by [12], called Multiple Switching Tables (MST-DPC), performs a real-time selection of the most suitable switching table among four alternatives depending on the operating conditions of the device.

The performance of conventional DPC under unbalanced and/or distorted input voltages deteriorates when this occurs. In recent years, researchers are increasingly interested in control methods to improve the performance of DPC strategies under unbalanced grid voltage conditions. Most solutions to improve the performance of three-phase Pulse Width Modulation (PWM) rectifiers are based on the extraction of the

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positive/negative voltage/current sequences. These solutions are complicated and difficult to implement for real-time applications since they require a great deal of computation.

In Virtual Flux-based (VF) methods, the VF space vector is obtained by integrating the voltage space vector. The use of a pure integrator induces a shift of the direct current in the estimated VF in order to remedy this drawback. Several methods of practical implementation of VF estimation have been proposed, with the aim of avoiding saturation of the estimated signals. This integration is generally performed via a First Order Low Pass (FLOP) filter to avoid the saturation and DC drift problems associated with pure integrators [13]. The DPC Control Based Virtual Flux (VF-DPC) uses the detected AC line currents and the estimated VF to calculate the input powers. However, FLOP filters lead to amplitude and phase errors. Although these errors can be minimized by reducing the cut-off frequency of the filter, this reduction leads to a reduction in the passband of the filter and therefore to a degradation of its dynamics. And for that, the authors [14, 15] propose a second order generalized integrator (SOGI) estimator exploiting the concept of virtual flow. Recent research in [16] presents a combination of two cascade filters to achieve a VF-based timing scheme, to ensure that the gate currents emulate the desirable sine waveform even when the supply voltage is unbalanced and/or harmonized.

Although the Grid Voltage Modulated Direct Power Control (GVM-DPC) is first introduced in [17], the main contribution of this paper is the proposal for an improvement in the technique using VF incorporated in a DPC scheme (GVM-DPC), for a three-phase PWM rectifier in our case. The results of the simulation of VF-GVM-DPC are compared with GVM-DPC under a polluted network which gives better performance than the classical method. The VF algorithm has an open loop structure and uses the fundamental orthogonal output signals which are obtained directly from the estimation of the fundamental active and reactive powers. This method provides quasi-sinusoidal input current waveform under different input voltage condition and achieves good stability, improves the performance of GVM-DPC.

The goal of the paper is the mathematical analysis and the numerical implementation of an improved method of grid voltage modulated based on direct power control for three phase pulse width modulation rectifiers.

The rest of the paper is organized as follows. In Section 2 the system modeling and the design of the GVM-DPC controls are presented. Section 3 shows the simulation results using MATLAB/Simulink. Finally, conclusions are given in the last Section 4.

2. Virtual flux grid voltage modulated direct power control.

2.1. Modeling VSC.

Figure 1 shows a simplified circuit of a two-level VSC connected to the grid with an LR filter all of these are considered ideal switches. The DC side could be connected to renewable energy sources or energy storage systems with a capacitor C and even at transmission scale High Voltage DC.

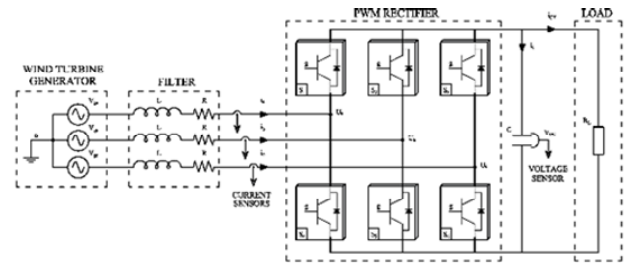


Fig. 1. Representation of a VSC connected to wind turbine generator

The relationship among the VSC output voltages, the grid voltages, and the output currents in the stationary reference frame by using the Clark transformation can be expressed as:

$$\begin{cases} U_{\alpha} = R \cdot i_{\alpha} + L \cdot \frac{di_{\alpha}}{dt} + V_{g\alpha}; \\ U_{\beta} = R \cdot i_{\beta} + L \cdot \frac{di_{\beta}}{dt} + V_{g\beta}, \end{cases} \quad (1)$$

where U_{α} and U_{β} indicate the VSC output voltage; i_{α} and i_{β} indicate the output currents; $V_{g\alpha}$ and $V_{g\beta}$ indicate the grid voltage in $\alpha\beta$ frame, and L and R are the filter inductance and resistance, respectively.

2.2. Modeling VF.

The concept of VF is based on the voltage integral and can be applied as an estimation method for voltage-sensor-less control of VSCs:

$$\Psi_{\alpha\beta} = \int U_{\alpha\beta} dt = \int \left(R \cdot i_{\alpha\beta} + L \cdot \frac{di_{\alpha\beta}}{dt} + V_{g\alpha\beta} \right) dt, \quad (2)$$

where $\Psi_{\alpha\beta}$ is the estimated VF.

The use of the proposed Dual Virtual Flux Phase Locked Loop (DVF-PLL), based on the cascade of two adaptive filters illustrated in Fig. 2, is an optimized solution.

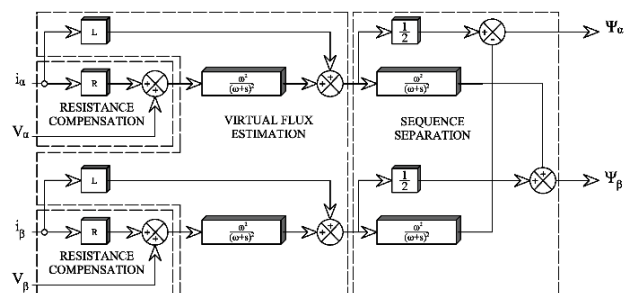


Fig. 2. The diagram of the proposed Dual Virtual Flux estimator

The DVF-PLL filter function is based on the estimated voltage and measurements current. First, the voltage reference signals are expressed in the stationary reference frame ($\alpha\beta$). Then the resistive voltage drop included in the model is subtracted. The latter is shifted in phase (90°) and gains unity for the fundamental frequency used to estimate the VF components with the second-order-low-pass filters. The estimated components of the VF are separated in positive and negative sequences with the same transfer function as that used for the estimation of the VF components. By using the estimated VF components in the stationary reference frame, the flux angles can be estimated by using a conventional PLL. The cutoff frequency is equal to 50 Hz and only the positive

sequence is used to ensure adequate operation under faulty network conditions [16].

2.3 Fuzzy Controller for PLL. To have good PLL control performance, especially in case of voltage disturbance variation and load disturbance the PI controller will be replaced by a fuzzy controller. The schematic diagram of this control is given in Fig. 3.

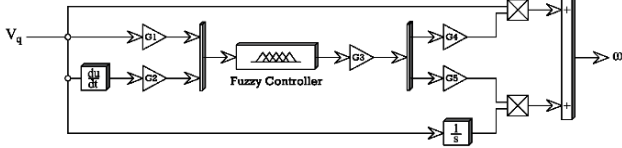


Fig. 3. The PLL fuzzy control

The fuzzy regulator uses two inputs. The first input is the error between the reference and the measured value of the quadratic voltage V_q . The second one represents the variation of this error.

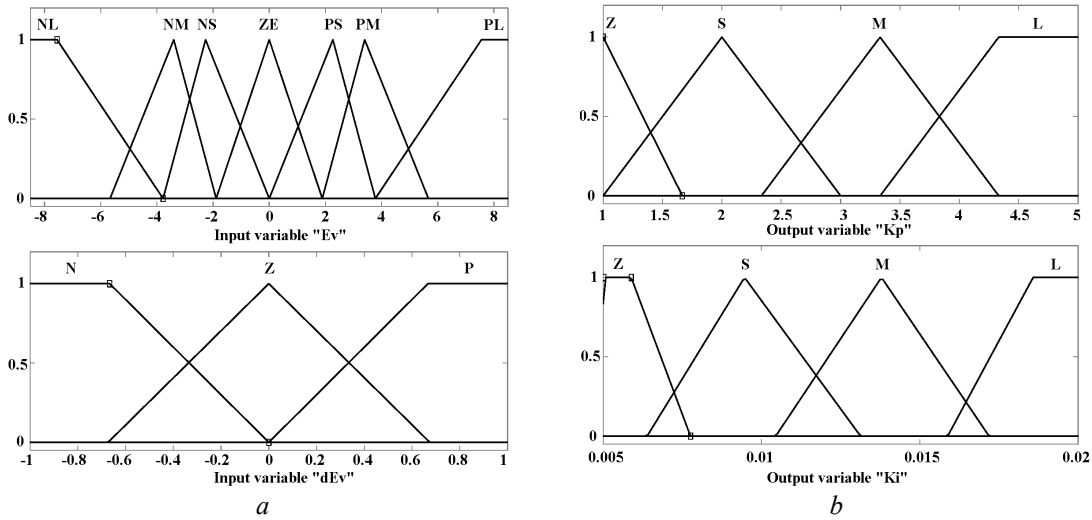


Fig. 4. The fuzzy membership functions of input (a) and output (b) variables

Fuzzy rules

KP		Eω						
		NL	NM	NS	ZE	PS	PM	PL
ΔE	N	L	M	S	M	S	M	L
	Z	Z	S	M	L	M	S	Z
ΔE	Z	L	M	L	Z	L	M	L
	P	Z	S	M	L	M	S	Z
ΔE	P	L	M	L	Z	L	M	L
	Z	Z	M	L	L	L	M	Z

2.4 Modeling DPC. Line current and VF components are used in power calculations. It should be noted that electrical resistance is included in the estimation process VF, they can be calculated by the following expressions:

$$\begin{cases} P = \omega \cdot (\Psi_\alpha \cdot i_\beta - \Psi_\beta \cdot i_\alpha) \\ Q = \omega \cdot (\Psi_\alpha \cdot i_\alpha + \Psi_\beta \cdot i_\beta) \end{cases} \quad (4)$$

where P is the estimate active power; Q is the estimate reactive power; ω is the angular frequency of the grid fundamental wave; Ψ_α , Ψ_β , i_α , i_β are respectively the virtual flow and the currents in the reference $\alpha\beta$.

By deriving (3) with respect to time, instantaneous variations in active and reactive power dP/dt and dQ/dt

These two signals are expressed by:

$$\begin{cases} e = V_{qref}(n) - V_q(n); \\ \Delta e = e(n) - e(n-1), \end{cases} \quad (3)$$

where e and Δe are the error of the quadratic voltage and its variation of the error.

For fuzzification, we used triangular membership functions for the error. We chose the seven fuzzy sets: NL (negative large), NM (negative middle), NS (negative small), ZE (zero), PS (positive small), PM (positive middle), PL (positive large). The rate of change $\Delta E\omega$ includes 3 fuzzy subsets, it is not necessary to subdivide it, because it is changing quickly in DPC. Output membership KP and KI, both contain four fuzzy subsets as shown in Fig. 4. There are total of 21 rules as listed in Table 1.

respectively can be expressed as a function of variations in network voltage and output current as follows:

$$\begin{cases} \frac{dP}{dt} = i_\beta \cdot \frac{d\Psi_\alpha}{dt} + \Psi_\alpha \cdot \frac{di_\beta}{dt} - i_\alpha \cdot \frac{d\Psi_\beta}{dt} - \Psi_\beta \cdot \frac{di_\alpha}{dt}; \\ \frac{dQ}{dt} = i_\alpha \cdot \frac{d\Psi_\alpha}{dt} + \Psi_\alpha \cdot \frac{di_\alpha}{dt} + i_\beta \cdot \frac{d\Psi_\beta}{dt} - \Psi_\beta \cdot \frac{di_\beta}{dt}. \end{cases} \quad (5)$$

If we consider a non-distorted grid, the following relationship could be obtained

$$\begin{cases} \frac{d\Psi_\alpha}{dt} = -\omega \cdot \Psi_\beta; \\ \frac{d\Psi_\beta}{dt} = \omega \cdot \Psi_\alpha, \end{cases} \quad (6)$$

where $\omega = 2\pi \cdot f$ is the angular frequency of the grid voltage and f is the frequency of the grid voltage.

By substituting (4) in (5), the state-space model of the active and reactive powers is obtained as follows:

$$\begin{cases} \frac{dP}{dt} = -\frac{R}{L} \cdot P - \omega \cdot Q + \frac{1}{L} \cdot (\omega \cdot (\Psi_\alpha \cdot U_\beta + \Psi_\beta \cdot U_\alpha) - \Psi_g^2) \\ \frac{dQ}{dt} = \omega \cdot P - \frac{R}{L} \cdot Q + \frac{\omega}{L} \cdot (\Psi_\alpha \cdot U_\beta + \Psi_\beta \cdot U_\alpha) \end{cases} \quad (7)$$

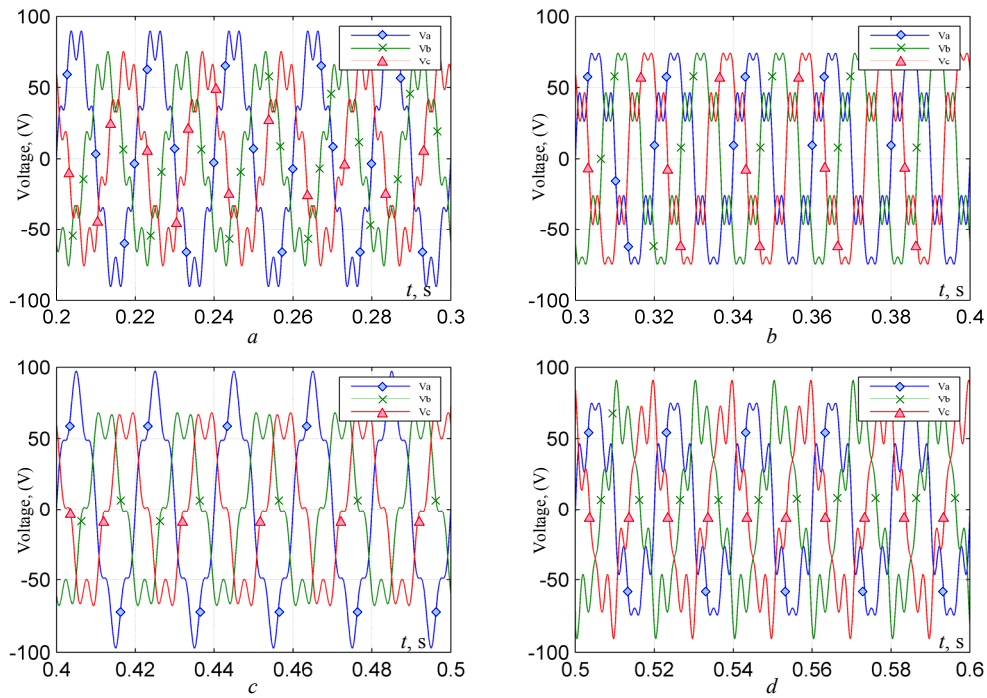


Fig. 6. The different voltage source condition applied to the rectifier:
 (a) – unbalanced and harmonic 7 to 20 %; (b) – harmonic 5 and 7 to 20 %;
 (c) – unbalanced and harmonic 5 to 20 %; (d) – harmonic 5 and 7 to 20 % asymmetric;

Table 2

System parameters used in simulation

Parameters	Symbol	Values	Parameters	Symbol	Values	Parameters	Symbol	Values
The resistance of reactors	R	0.56 Ω	DC-bus capacitor	C	1100 μF	Switching frequency	F_{sw}	7500 Hz
Load resistance	RL	68.6 Ω	The line-to-line AC voltage	E	85 V	DC-bus voltage	VDC	180 V
Inductance of reactors	L	19.5 mH	Frequency	F	50 Hz	Sampling period	T_s	20 μs

Figure 7 shows the results for GVM-DPC:

- (a) – the bus voltage curve;
- (b) – the grid current;
- (c) – the estimated voltages $U_{\alpha\beta}$;

- (d) – the superimposition current i and voltage V in phase A;
- (e) – the active P and reactive Q powers.

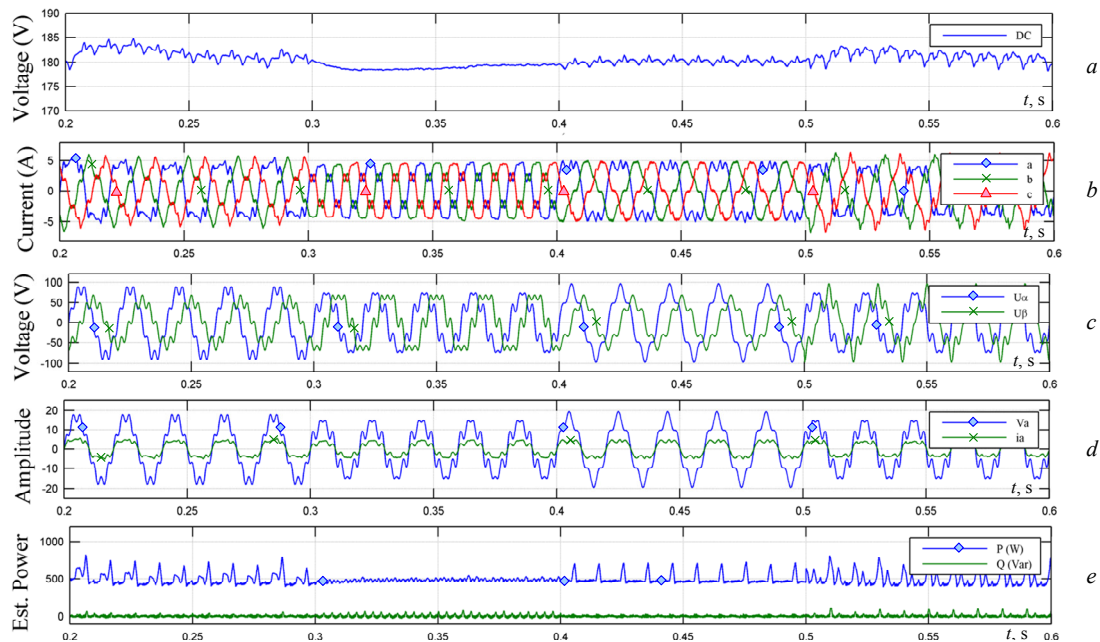


Fig. 7. The simulation results GVM-DPC

Figure 8 shows the results for VF-GVM-DPC:

- (a) – the bus voltage curve;
- (b) – the grid current;
- (c) – the estimated virtual flux $\Psi_{\alpha\beta}$;

- (d) – the superposition current i and the voltage V in phase A;
- (e) – the active P and reactive Q powers.

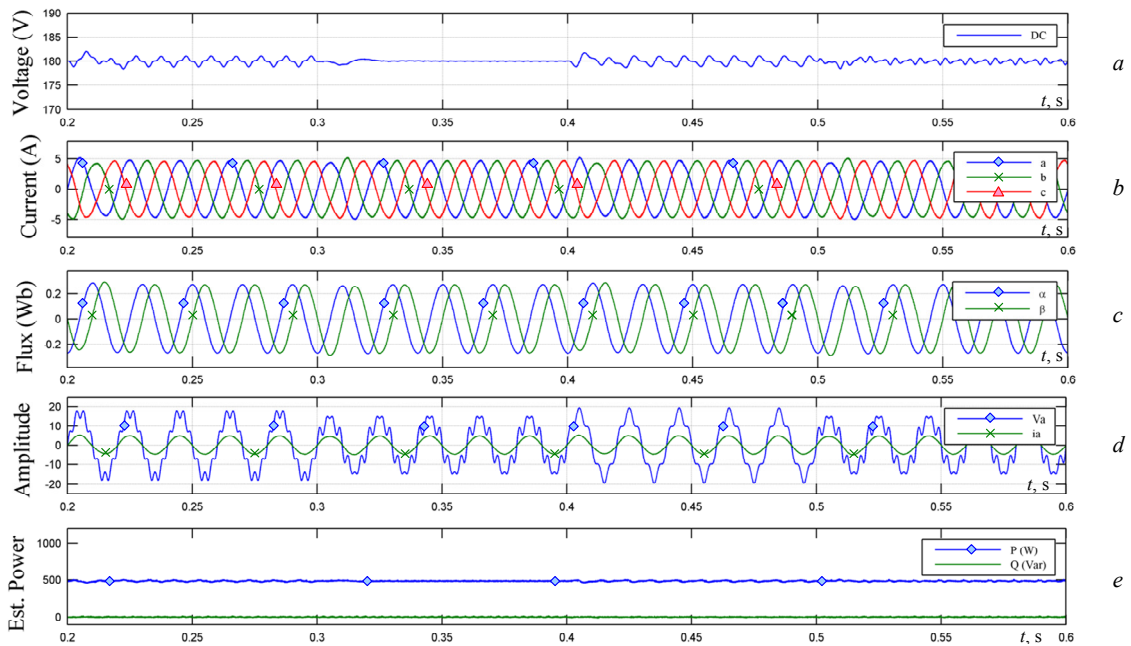


Fig. 8. The simulation result VF-GVM-DPC

The distribution of Fig. 7 is the same as that of Fig. 8. We can see in Fig. 7 the GVM-DPC which leads to fluctuations in the voltage curve of the bus. In addition, the grid current is affected by disturbed grid voltage conditions, with a high THD rate, which reduces the accuracy of the estimates of active and reactive power as can be seen in Fig. 7. In addition, the DC bus seriously oscillates around 5 V and the system does not operate under a unit power factor which is a very important criterion during our comparison.

On the other hand, we can see in Fig. 8 VF-GVM-DPC which gives us the current and the voltage are in phase. Although there are still some low order harmonics, we can observe that they have a limited influence compared to that of Fig. 7. The oscillations of the vector component of the estimated flux are sinusoidal and out of phase without disturbance and fluctuation.

Figure 9 shows the bus voltage stabilizers at about minus 2 V and the system is operating at unity power factor.

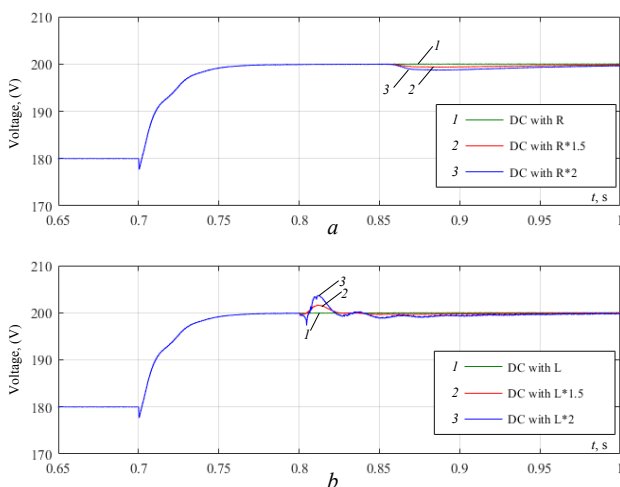


Fig. 9. System parameters influence on the evolution of the DC bus voltage: R (a); L (b)

Figure 10 shows the difference between the application of an SRF-PLL and a PLL which operates on the basis of a fuzzy logic controller to detect the fundamental frequency positive sequence component of the mains voltage in unbalanced and distorted conditions as well as fast and smooth tracking.

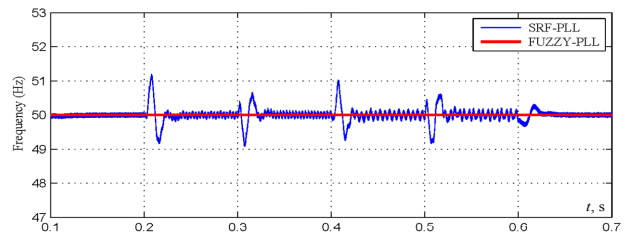


Fig. 10. The difference between Synchronous Reference Frame based Phase Locked Loop and proposed fuzzy-PLL

From Fig. 11 we can note that the THD rate of the network current has been improved compared to traditional control while respecting the standard of the electrical network. To highlight the contribution of the oscillatory terms, first of all, the results of the proposed control structure are shown in Fig. 2. The design and optimization process were carried out in the same manner as the procedure presented above. This result expressly shows how the components of the low order harmonics (5 and 7) with symmetrical and asymmetrical fault of the network voltage affect the network current. To summarize the results, Fig. 11 below presents an analysis based on the current THD of the VF-GVM-DPC strategies studied in this article.

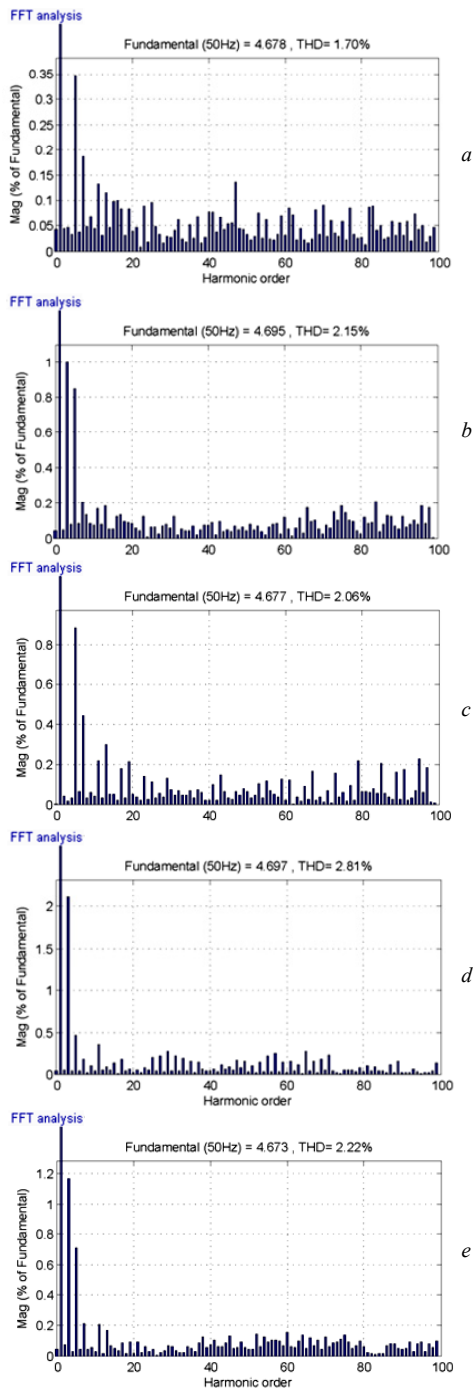


Fig. 11. Fast Fourier Transform analysis showing THD in source current of the proposed method:

- a) ideal network voltage conditions;
- b) unbalance and injection of the 7th order harmonic;
- c) injection harmonics 5 and 7;
- d) an asymmetrical disturbance with harmonics 5;
- e) an asymmetrical disturbance with harmonics 5 and 7

4. Conclusions.

This paper presented the mathematical analysis and the numerical implementation of an improved method of Grid Voltage Modulated based on Direct Power Control or we introduced them for three phase rectifiers. The main objectives of the proposed control strategy are to obtain sinusoidal input currents under different input voltage conditions and to maintain the DC bus voltage at the level required as part of the improvement of the wind chain.

In these situations, harmonic components appear on the grid voltage, which causes distortion and current imbalance if the power reference is kept constant. However, the use of the proposed Virtual Flux-Grid Voltage Modulated method has the same control structure as the classic Grid Voltage Modulated, except for the use of virtual flux instead of voltages, which results in the optimization of the cost, since the voltage sensors will not be used, which is based on the principle of disturbance rejection, makes it possible to obtain an input current of low THD compared to the conventional method.

The proposed control method could offer many advantages and allows to obtain good performances with a THD of 1.7 % under ideal conditions and during disturbances it varies between 2.06 % to 2.81 % which respects the standard, without forgetting with a load lower calculation, without it being necessary to specify the power calculation term, nor to extract the positive / negative voltage sequence. We can therefore say that this control method is able to improve the quality of the input current.

Conflict of interest. The authors declare that they have no conflicts of interest.

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